

| | Type | Hits | Search Text | DBs |
|----|------|-------|--|---|
| 1 | BRS | 16267 | microstrip\$1 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 2 | BRS | 2486 | (integrated adj circuit) and microstrip\$1 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 3 | BRS | 0 | (integrated adj circuit) and (substrate same microstrip\$1) and ((signal adj pad) same (external adj terminal\$1) same microstrip\$1) and (microstrip\$1 same (transmitting adj signal\$1)) and (microstrip\$1 same capacitively same coupled) and (microstrip\$1 same (constant near3 impedance)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 4 | BRS | 0 | ((signal adj pad\$1) same (external adj terminal\$1) same microstrip\$1) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 5 | BRS | 11 | ((signal adj pad\$1) same microstrip\$1) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 6 | BRS | 0 | (substrate same plurality same microstrip\$1) and (microstrip\$1 same (transmitting adj signal\$1)) and (microstrip\$1 same capacitively same coupled) and (microstrip\$1 same (constant near3 impedance)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 7 | BRS | 517 | substrate same plurality same microstrip\$1 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 8 | BRS | 109 | substrate same plurality near2 microstrip\$1 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 9 | BRS | 0 | (substrate same plurality same lead\$1) and (lead\$1 same (transmitting adj signal\$1)) and (lead\$1 same capacitively same coupled) and (lead1 same (constant near3 impedance)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 10 | BRS | 0 | (integrated adj circuit) and (substrate same lead\$1) and ((signal adj pad) same (external adj terminal\$1) same lead\$1) and (lead\$1 same (transmitting adj signal\$1)) and (lead\$1 same capacitively same coupled) and (lead\$1 same (constant near3 impedance)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 11 | BRS | 149 | microstrip\$1 same capacitively same coupled | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |

| | Time Stamp | Comments | Error Definition | Errors |
|----|---------------------|----------|------------------|--------|
| 1 | 2003/02/21 14:31 | | | 0 |
| 2 | 2003/01/23 16:04 | | | 0 |
| 3 | 2003/01/23 16:22 | | | 0 |
| 4 | 2003/01/23 16:06 | | | 0 |
| 5 | 2003/01/23 16:07 | | | 0 |
| 6 | 2003/01/23 16:26 | | | 0 |
| 7 | 2003/01/23 16:16 | | | 0 |
| 8 | 2003/01/23 16:16 | | | 0 |
| 9 | 2003/01/23 16:22 | | | 0 |
| 10 | 2003/01/23 16:24 | | | 0 |
| 11 | 2003/01/23 16:26 | | | 0 |

| | Type | Hits | Search Text | DBs |
|----|------|------|--|---|
| 12 | BRS | 9 | microstrip\$1 same (rotat\$3 with outward\$2) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 13 | BRS | 765 | microstrip\$1 and substrate with alumina | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 14 | BRS | 343 | microstrip\$1 same substrate with alumina | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 15 | BRS | 0 | microstrip\$1 and demultiplexor same multiplexor same "OC-768" | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 16 | BRS | 4 | microstrip\$1 and demultiplexor same multiplexor | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 17 | BRS | 1 | microstrip\$1 same substrate near3 alumina and (coaxial adj terminals) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 18 | BRS | 1 | microstrip\$1 and substrate near3 alumina and (coaxial adj terminals) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 19 | BRS | 15 | microstrip\$1 and (coaxial adj terminals) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 20 | BRS | 241 | microstrip\$1 same substrate near3 alumina | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 21 | BRS | 2 | microstrip\$1 with widen\$3 with capacitance | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 22 | BRS | 0 | microstrip\$1 and GPP0 adj connectors | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 23 | BRS | 232 | microstrip\$1 near5 \$3ohm | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 24 | BRS | 0 | microstrip\$1 and (coaxial adj terminals) with ring | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 25 | BRS | 27 | microstrip\$1 near5 "50-ohm" | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 26 | BRS | 1 | (microstrip\$1 with high adj speed) and (microstrip\$1 with low adj speed) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 27 | BRS | 59 | microstrip\$1 and (ball adj grid adj array) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 28 | BRS | 293 | microstrip\$1 same substrate near3 ceramic | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |

| | Time Stamp | Comments | Error Definition | Errors |
|----|---------------------|----------|------------------|--------|
| 12 | 2003/02/03 15:58 | | | 0 |
| 13 | 2003/02/03 16:01 | | | 0 |
| 14 | 2003/02/03 16:00 | | | 0 |
| 15 | 2003/02/03 16:02 | | | 0 |
| 16 | 2003/02/03 16:02 | | | 0 |
| 17 | 2003/02/03 16:05 | | | 0 |
| 18 | 2003/02/03 16:17 | | | 0 |
| 19 | 2003/02/03 16:28 | | | 0 |
| 20 | 2003/02/03 16:34 | | | 0 |
| 21 | 2003/02/03 17:22 | | | 0 |
| 22 | 2003/02/03 16:18 | | | 0 |
| 23 | 2003/02/03 16:19 | | | 0 |
| 24 | 2003/02/03 16:20 | | | 0 |
| 25 | 2003/02/03 16:20 | | | 0 |
| 26 | 2003/02/03 16:25 | | | 0 |
| 27 | 2003/02/03 16:28 | | | 0 |
| 28 | 2003/02/03 16:37 | | | 0 |

| | Type | Hits | Search Text | DBs |
|----|------|------|---|---|
| 29 | BRS | 52 | microstrip\$1 and differential adj signals | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 30 | BRS | 228 | microstrip\$1 and substrate same interconnect\$3 same via | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 31 | BRS | 25 | microstrip\$1 and substrate same interconnect\$3 same via same conductor same dielectric same ground | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 32 | BRS | 0 | microstrip\$1 and high?speed adj differential adj signals | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 33 | BRS | 0 | microstrip\$1 and high?speed near4 data near3 signals with frequency | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 34 | BRS | 94 | microstrip\$1 and high?speed with frequency | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 35 | BRS | 1 | microstrip\$1 and high?speed with frequency with gbps | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 36 | BRS | 54 | microstrip\$1 with wid\$5 with increase | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 37 | BRS | 2 | microstrip\$1 with wid\$5 with increase with capacit\$4 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 38 | BRS | 0 | microstrip\$1 and (substrate same dielectric) and (dielectric same co?fired adj ceramics adj process) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 39 | BRS | 0 | (substrate same dielectric) and (dielectric same co?fired adj ceramics adj process) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 40 | BRS | 0 | dielectric same co?fired adj ceramics adj process | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 41 | BRS | 4 | dielectric same ceramics adj process | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 42 | BRS | 0 | co?fired adj ceramics adj process | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 43 | BRS | 2 | 5307237.pn. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 44 | BRS | 171 | (microstrip\$1 same (constant near3 impedance)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |

| | Time Stamp | Comments | Error Definition | Errors |
|----|---------------------|----------|------------------|--------|
| 29 | 2003/02/03 17:13 | | | 0 |
| 30 | 2003/02/03 16:51 | | | 0 |
| 31 | 2003/02/03 17:30 | | | 0 |
| 32 | 2003/02/03 17:13 | | | 0 |
| 33 | 2003/02/03 17:15 | | | 0 |
| 34 | 2003/02/03 17:19 | | | 0 |
| 35 | 2003/02/03 17:19 | | | 0 |
| 36 | 2003/02/03 17:25 | | | 0 |
| 37 | 2003/02/03 17:27 | | | 0 |
| 38 | 2003/02/03 17:31 | | | 0 |
| 39 | 2003/02/03 17:31 | | | 0 |
| 40 | 2003/02/03 17:32 | | | 0 |
| 41 | 2003/02/03 17:32 | | | 0 |
| 42 | 2003/02/03 17:32 | | | 0 |
| 43 | 2003/02/21 14:02 | | | 0 |
| 44 | 2003/02/21 14:05 | | | 0 |

| | Type | Hits | Search Text | DBs |
|----|------|------|--|---|
| 45 | BRS | 10 | microstrip\$1 near3 (first adj width) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 46 | BRS | 462 | 438/122.ccls. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 47 | BRS | 819 | 438/123.ccls. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 48 | BRS | 519 | 438/121.ccls. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 49 | BRS | 2076 | 438/106.ccls. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 50 | BRS | 1364 | microstrip with width | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 51 | BRS | 111 | microstrip with width with (chang\$3 or different) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 52 | BRS | 6 | microstrip with width with (chang\$3 or different) same capacit\$4 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 53 | BRS | 30 | microstrip with width with capacitance | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 54 | BRS | 1 | (dielectric adj ring) with thickness with substrate | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 55 | BRS | 549 | dielectric adj ring | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 56 | BRS | 0 | (dielectric adj ring) with ((external or coaxial) adj terminal\$1) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 57 | BRS | 28 | (dielectric adj ring) with coaxial | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |

| | Time Stamp | Comments | Error Definition | Errors |
|----|---------------------|----------|------------------|--------|
| 45 | 2003/02/21 14:33 | | | 0 |
| 46 | 2003/03/07 11:19 | | | 0 |
| 47 | 2003/03/07 11:19 | | | 0 |
| 48 | 2003/03/07 11:19 | | | 0 |
| 49 | 2003/03/07 11:59 | | | 0 |
| 50 | 2003/03/07 12:08 | | | 0 |
| 51 | 2003/03/07 12:24 | | | 0 |
| 52 | 2003/03/07 12:28 | | | 0 |
| 53 | 2003/03/07 12:28 | | | 0 |
| 54 | 2003/03/07 12:42 | | | 0 |
| 55 | 2003/03/07 12:40 | | | 0 |
| 56 | 2003/03/07 12:43 | | | 0 |
| 57 | 2003/03/07 12:44 | | | 0 |